



LOGI-TRENNVERST-18

Hardware-Description

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INDEX

<u>1. Introduction</u>	4
<u>1.1. General remarks</u>	4
<u>1.2. Customer satisfaction</u>	4
<u>1.3. Customer response</u>	4
<u>2. Hardware description</u>	6
<u>2.1. Overview screen</u>	6
<u>2.2. Technical data</u>	7
<u>2.3. Mechanics</u>	8
<u>2.4. Pin assignment plug-in connector (20pol.)</u>	9
<u>2.4.1. J1- Output</u>	9
<u>2.4.2. J2 - Input</u>	10
<u>2.5. J3 - USB</u>	11
<u>3. Appendix</u>	13
<u>3.1. Revisions</u>	13
<u>3.2. Copyrights and trademarks</u>	14



Introduction



1. Introduction

1.1. General remarks

First of all, we would like to congratulate you to the purchase of a high quality DEDITEC product.

Our products are being developed by our engineers according to quality requirements of high standard. Already during design and development we take care that our products have -besides quality- a long availability and an optimal flexibility.

Modular design

The modular design of our products reduces the time and the cost of development. Therefore we can offer you high quality products at a competitive price.

Availability

Because of the modular design of our products, we have to redesign only a module instead of the whole product, in case a specific component is no longer available.

1.2. Customer satisfaction

Our philosophy: a content customer will come again. Therefore customer satisfaction is in first place for us.

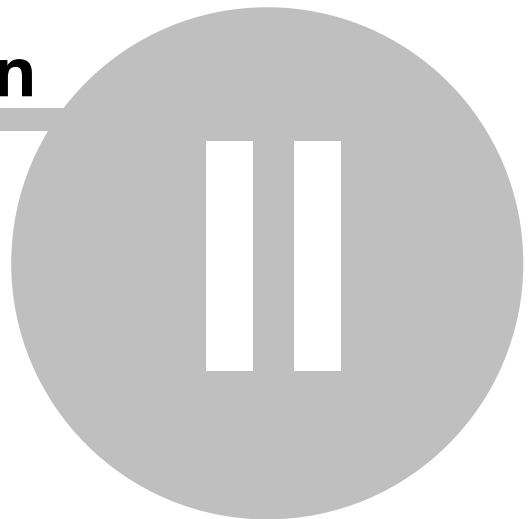
If by any chance, you are not content with the performance of our product, please contact us by phone or mail immediately.

We take care of the problem.

1.3. Customer response

Our best products are co-developments together with our customers. Therefore we are thankful for comments and suggestions.

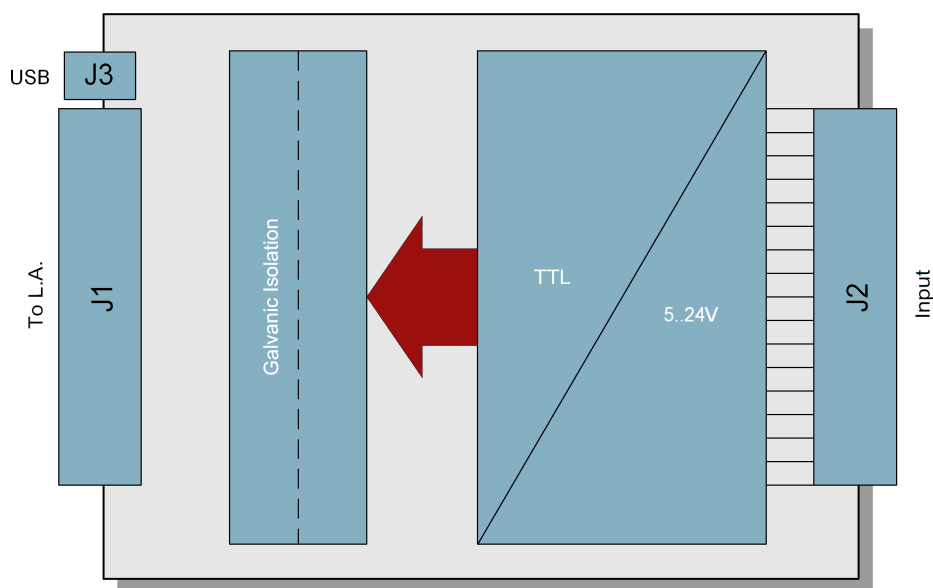
Hardware description



2. Hardware description

The "LOGI-TRENNVERST-18" allows to connect up to 18 input channels with a voltage level of 5V-24V directly to the logic analyzer. In addition the signals to the logic analyzer will be **galvanic isolated**.

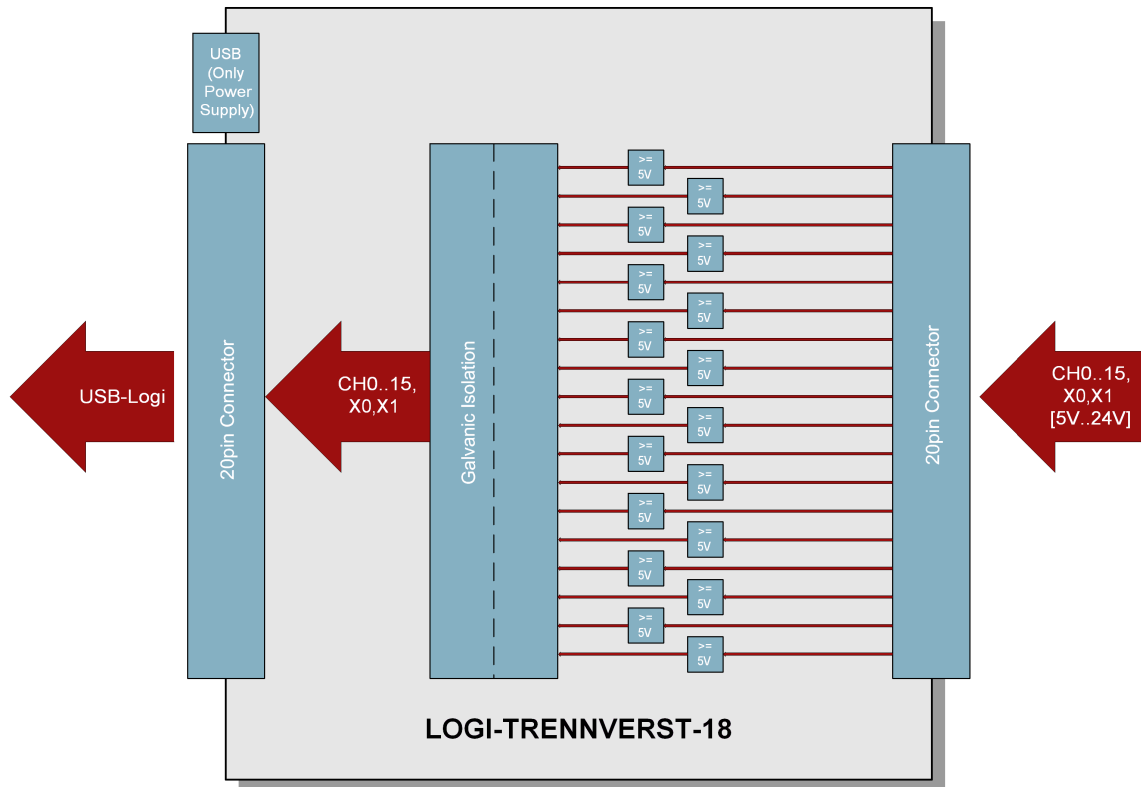
2.1. Overview screen



2.2. Technical data

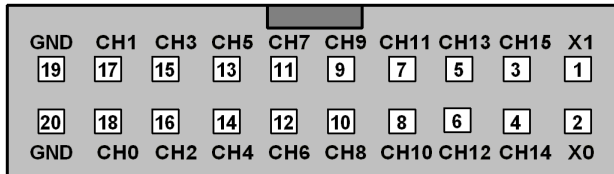
- 18 input channels
- Sensing frequencies up to 1 MHz
- Galvanic isolation of all input channels
- Measurable voltage level of 5V-24V
- Power supply via USB
- Compatible to all DEDITEC logic analyzers
- Dimension: 80x61x22mm (LxWxH)

2.3. Mechanics



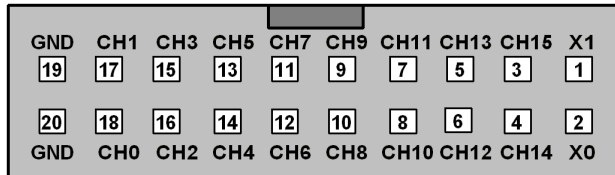
2.4. Pin assignment plug-in connector (20pol.)

2.4.1. J1- Output



Pin	Name
1	X1
2	X0
3	CH15
4	CH14
5	CH13
6	CH12
7	CH11
8	CH10
9	CH9
10	CH8
11	CH7
12	CH6
13	CH5
14	CH4
15	CH3
16	CH2
17	CH1
18	CH0
19	GND
20	GND

2.4.2. J2 - Input



Pin	Name
1	X1
2	X0
3	CH15
4	CH14
5	CH13
6	CH12
7	CH11
8	CH10
9	CH9
10	CH8
11	CH7
12	CH6
13	CH5
14	CH4
15	CH3
16	CH2
17	CH1
18	CH0
19	GND
20	GND

2.5. J3 - USB

The USB Port of the "**LOGI-TRENNVERST-18**" provides only the power supply for the module. The port has not anymore other functions.

Appendix



3. Appendix

3.1. Revisions

Rev 2.00

First issue

3.2. Copyrights and trademarks

Linux is registered trade-mark of Linus Torvalds.

Windows CE is registered trade-mark of Microsoft Corporation.

USB is registered trade-mark of USB Implementers Forum Inc.

LabVIEW is registered trade-mark of National Instruments.

Intel is registered trade-mark of Intel Corporation

AMD is registered trade-mark of Advanced Micro Devices, Inc.